



JP
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mu-Jing Li et al.

Title: METHOD AND APPARATUS FOR EFFICIENTLY LOCATING AND
AUTOMATICALLY CORRECTING CERTAIN VIOLATIONS IN A
COMPLEX EXISTING CIRCUIT LAYOUT

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SUBMISSION OF NEW FORMAL DRAWINGS

Transmitted herewith are three (3) sheets of new drawings containing Fig. 1; Fig. 2A-
Fig. 2E; and Fig. 3A-Fig. 3D, respectively.

CERTIFICATE OF MAILING OR TRANSMISSION

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5/13/04
Date

Respectfully submitted,

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